A METHOD FOR EXPANDING THE ADDRESSING CAPABILITY OF A PLURALITY OF REGISTERS AND APPARATUS FOR IMPLEMENTATION THEREOF

ABSTRACT OF THE DISCLOSURE

A microprocessor includes a plurality of blocks of registers, each block of registers having at least two registers. The microprocessor further includes a location register for selectively characterizing at least one of the blocks as a specified block of registers, and a control register for selecting at least one operation for the indicated block of registers. In one example of the invention, the control and location registers are two of the registers specified by the IEEE 802.3 standard.

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